

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
John L. Fulton, et al.)	Art Unit: 1792
Serial No: 10/783,249)	Examiner: Nicole R. Blan
Filed: 2/19/04)	Confirmation No.: 1416
For: Process and Apparatus for)	File No: 14138-B
Removing Residues from Semiconductor)	Date: April 21, 2008
Substrates)	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

(References – See attached US_IDS_Form_SB_08a form)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

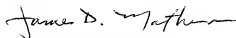
Attached hereto are Form SB08a listing documents believed to be relevant to the subject application. It is respectfully requested that these documents be considered by the examiner and an initialed copy of each form be returned to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made or that no other material information exists, as defined in 37 C. F. R. § 1.56(a). No admission is made regarding whether any of the submitted references is prior art.

It is believed that this disclosure complies with the requirements of 37 C. F. R. §§ 1.56, 1.97, 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

For the fee required, the Director is hereby authorized to charge our Deposit Account
No. 02-1275.

Respectfully submitted,

A handwritten signature in cursive script, appearing to read "James D. Matheson".

James D. Matheson
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